

SEMISTART

Antiparallel thyristors for softstart

SKKQ 800/18E

Features

- Compact design
- · Pressure contact technology

Typical Applications*

Soft starters

Remarks

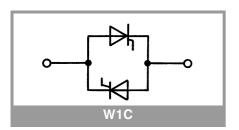
- Please note: This module has no soft mold protection around the chip. It is therefore susceptible to environmental influences (dust, humidity, etc.). The humidity test according to IEC60068-2-67 is not passed by this product.
- Recommendation: The devices should be installed in control cabinets of IP54 degree of protection.

Footnotes

 $^{1)}\,T_{jmax}$ up to 150°C is allowable for overload conditions, max. time period for the overload condition is 20s.

Absolute Maximum Ratings								
Symbol	Conditions		Values	Unit				
Module	·			•				
I _{overload}	W1C, sin. 180°, 20 s, T _{jmax} = 150 °C, T _{jstart} = 40 °C		800	Α				
I _{TSM}	10 ms	T _j = 25 °C	5700	Α				
		T _j = 125 °C	5200	Α				
i ² t	10 ms	T _j = 25 °C	162000	A ² s				
		T _j = 125 °C	135000	A ² s				
V_{RSM}			1900	V				
V _{RRM} V _{DRM}			1800	٧				
Tj			-40 + 125	°C				
T _{stg}			-40 + 125	°C				

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
					•		
V _T	T _j = 25 °C, I _T = 1000 A			1.9	V		
$V_{T(TO)}$	T _j = 125 °C			0.9	٧		
r _T	T _j = 125 °C			8.0	mΩ		
$I_{DD};I_{RD}$	$T_j = 125 ^{\circ}\text{C}$, $V_{RD} = V_{RRM}$, per module			80	mA		
t _{gd}	$T_j = 25 ^{\circ}\text{C}, I_G = 1 \text{A}, di_G/dt = 1 \text{A}/\mu \text{s}$		1		μs		
t _{gr}	$V_{D} = 0.67 * V_{DRM}$		2		μs		
(dv/dt) _{cr}	T _j = 125 °C		1000		V/µs		
(di/dt) _{cr}	T _j = 125 °C, f = 50 60 Hz		125		A/μs		
t _q	T _j = 125 °C		150		μs		
I _H	T _j = 25 °C		150	400	mA		
IL	$T_j = 25$ °C, $R_G = 33 \Omega$		300	1000	mA		
V_{GT}	T _j = 25 °C, d.c.	2			V		
I _{GT}	$T_j = 25$ °C, d.c.	150			mA		
V_{GD}	T _j = 125 °C, d.c.			0.25	V		
I_{GD}	T _j = 125 °C, d.c.			10	mA		
R _{th(j-r)}	continuous DC, per thyristor			0.106	K/W		
M _t	to terminals	4.25		5.75	Nm		
m	approx.		1200		g		
Case			2				



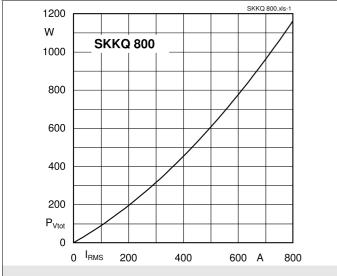


Fig. 1: Power dissipation per module vs. rms current

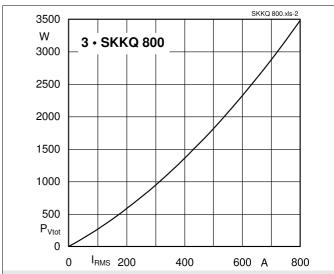


Fig. 2: Power dissipation of three modules vs. rms current

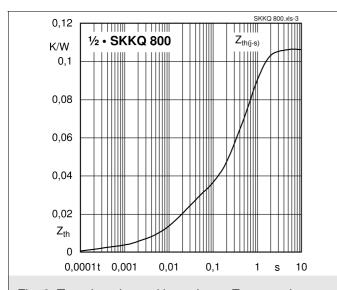


Fig. 3: Transient thermal impedance $Z_{th(j-r)}$ vs. time

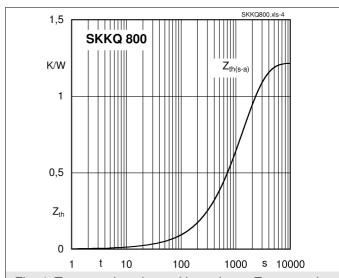


Fig. 4: Typ. transient thermal impedance $Z_{\text{th(s-a)}}$ vs. time (natural cooling)

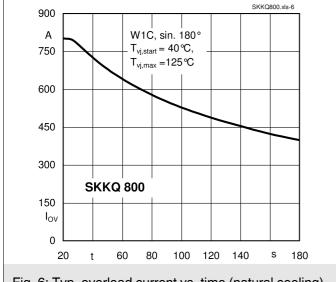


Fig. 6: Typ. overload current vs. time (natural cooling)

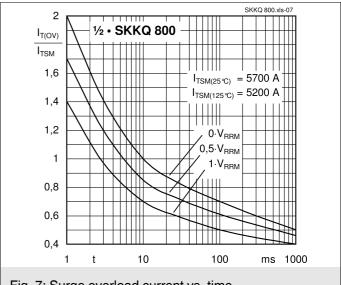
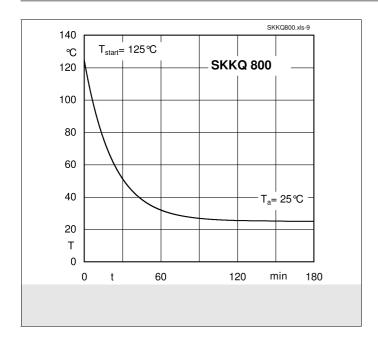
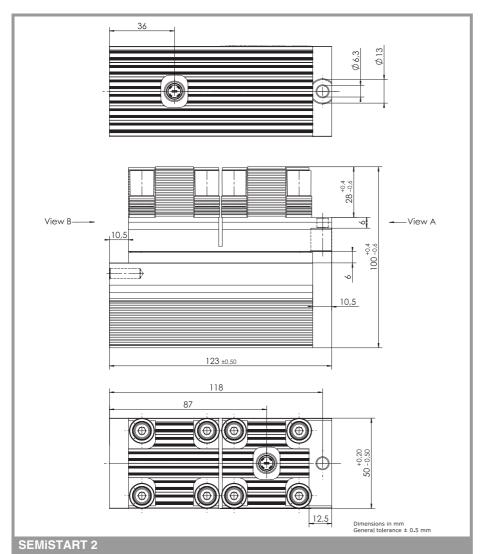
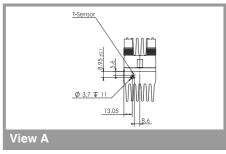
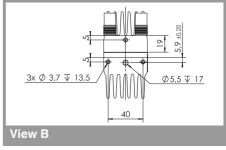


Fig. 7: Surge overload current vs. time









This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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